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2116

PATENT
30GF-1107

41



IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicant: Ronald E. Gareis, et al.

Serial No.: 09/751,233

Filed: December 28, 2000

For: METHOD AND APPARATUS
FOR REGULATING CURRENT
FOR PROGRAMMABLE LOGIC
CONTROLLERS

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: Art Unit: 2116
:
: Examiner: Paul B. Yanchus, III
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Technology Center 2100

AMENDMENT IN RESPONSE TO RESTRICTION REQUIREMENT

Mail Stop: Amendment
Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

Sir:

This Amendment is submitted in response to the Office Action dated June 30, 2004.